

FIG. 1 is a block diagram of a Single UFC System with 1 Frame Delay. The diagram illustrates the flow of video data from an input signal to an output signal, passing through a processing stage and a delay stage.

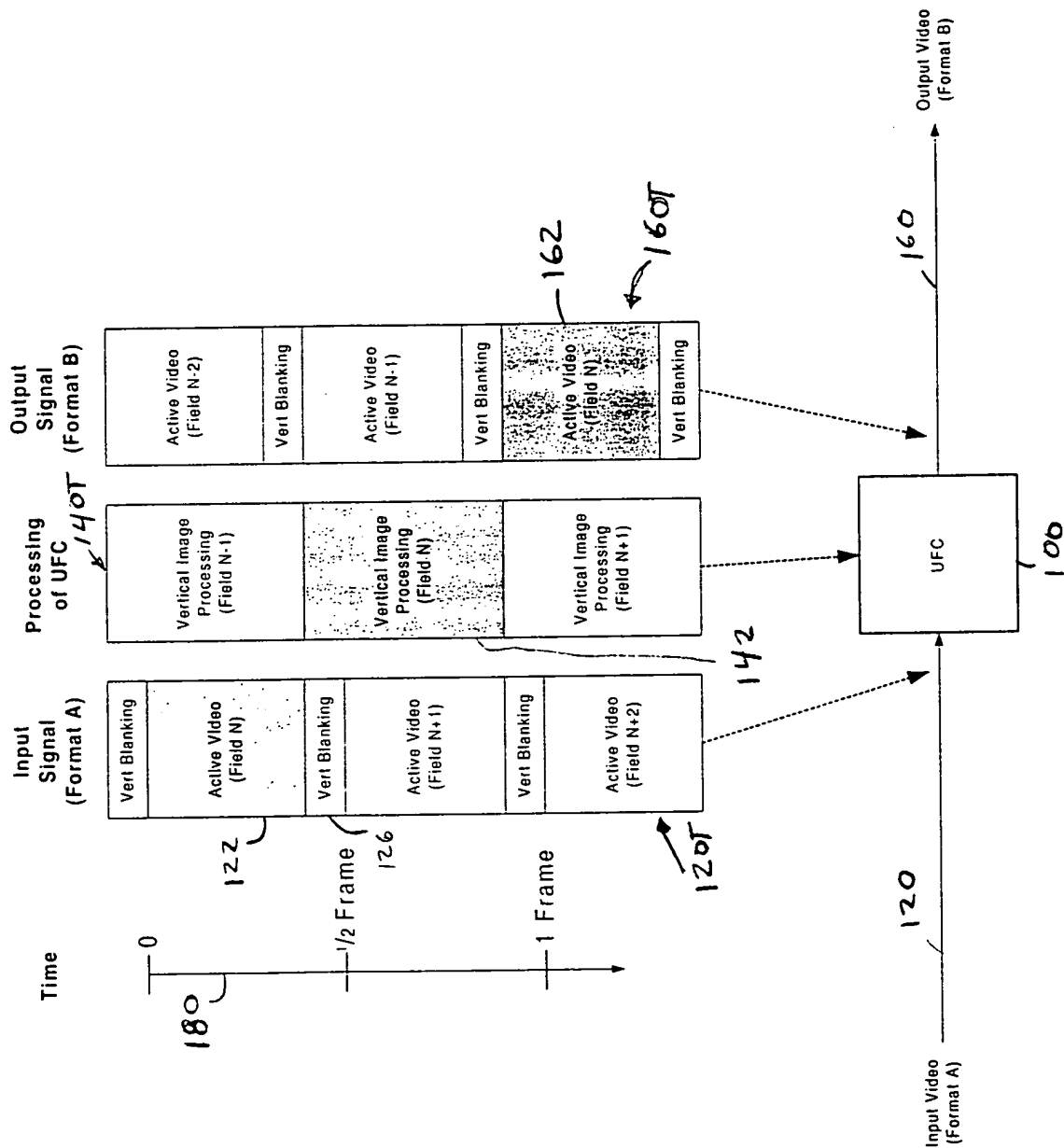


Figure 1 - Single UFC System with 1 Frame Delay

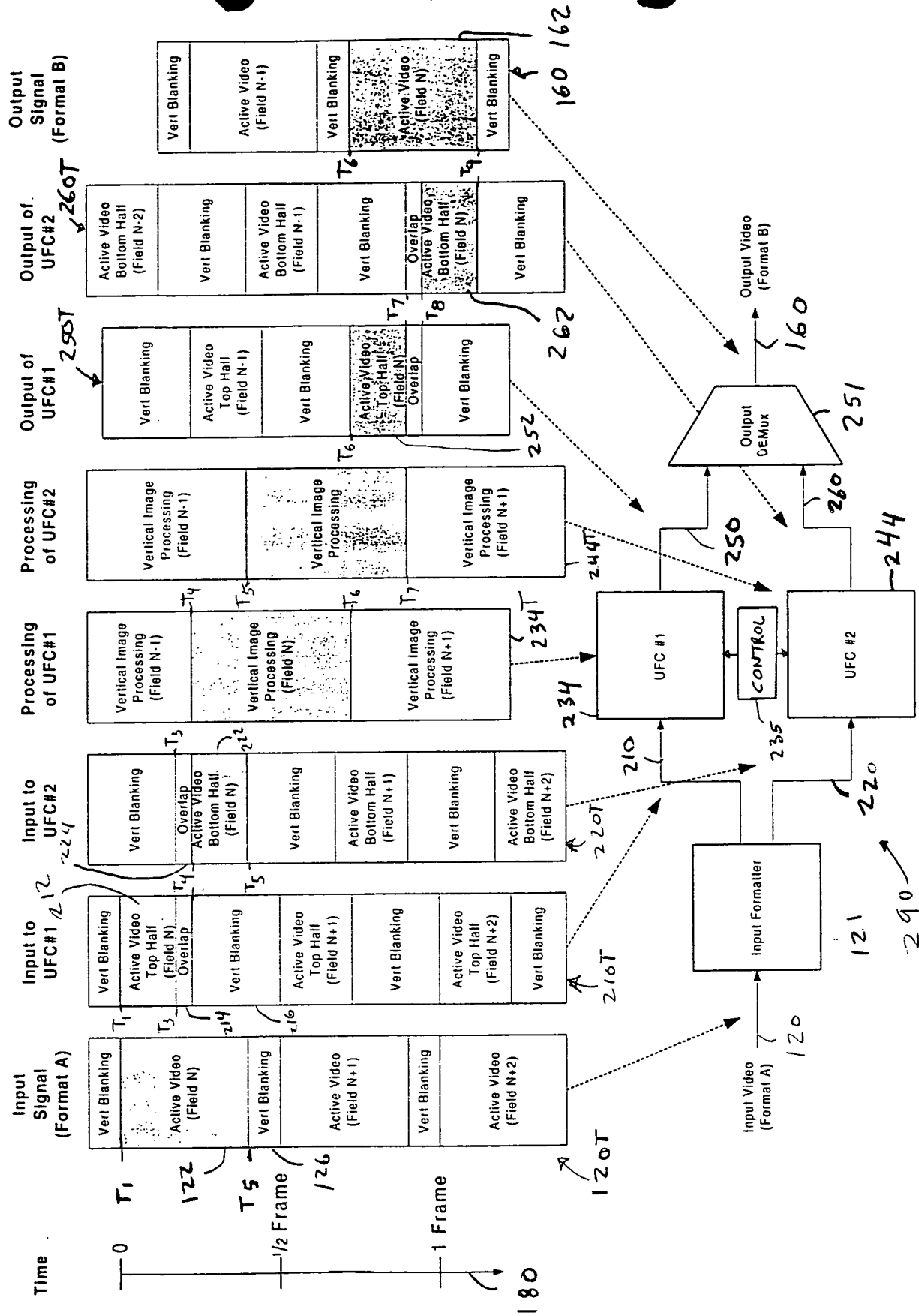


Figure 2 - Dual UFC System for Lower Delay

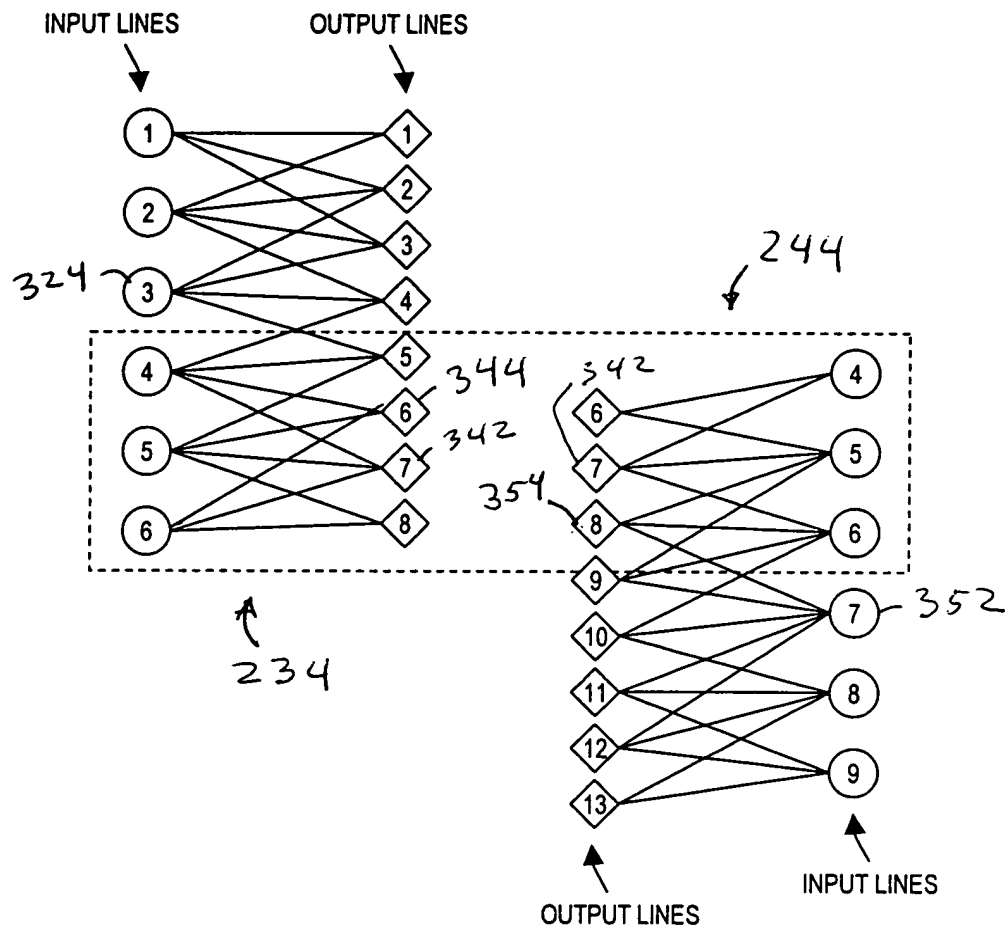


Fig. 3

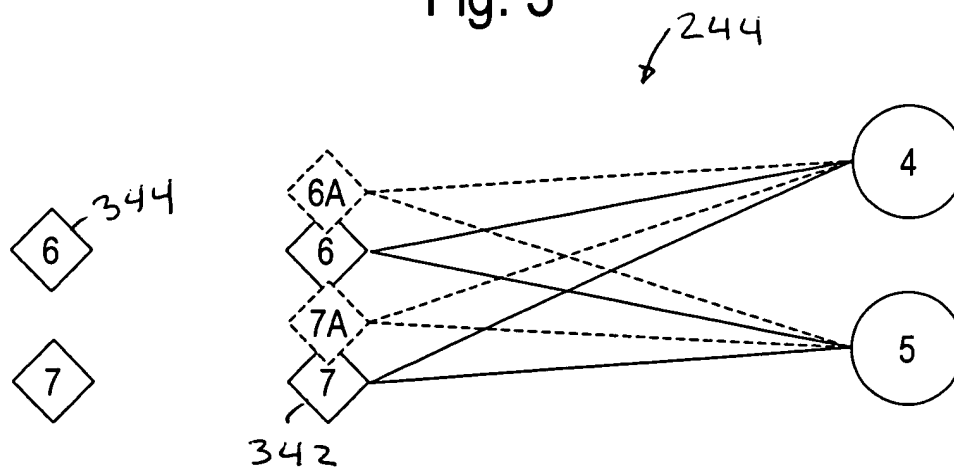


Fig. 3A